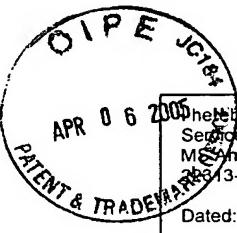
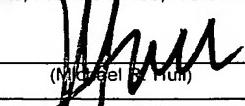


JFW



I hereby certify that this correspondence is being deposited with the U.S. Postal Service with sufficient postage as First Class Mail, in an envelope addressed to:
MS Amendment, Commissioner for Patents, P.O. Box 1450, Alexandria, VA
22313-1450, on the date shown below.

Dated: April 4, 2005

Signature: 

(Michael S. Huh)

Docket No.: 29925/39907
(PATENT)

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent Application of:
Seong-Wook Kim

Application No.: 10/762,818

Confirmation No.: 6202

Filed: January 22, 2004

Art Unit: 2818

For: METHOD FOR FABRICATING MERGED
LOGIC CMOS DEVICE

Examiner: Phuc T. Dang

AMENDMENT IN RESPONSE TO NON-FINAL OFFICE ACTION

MS Amendment
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Dear Sir:

INTRODUCTORY COMMENTS

In response to the office action mailed on January 4, 2005, please amend the above-identified U.S. patent application as follows:

Amendments to the Claims are reflected in the listing of claims which begins on page 2 of this paper.

Remarks/Arguments begin on page 4 of this paper.

Replacement Drawings are attached hereto as an appendix.